

11 11 11 10 FFF8h 01h 8 10 00 00 00 00 00 00 11 11 တ G-code data invalid invalid Buffer 8000h **73**h 22h 99 10 00 00 1021:1201200 00 00 00 00 001:1201 10 00 00 00 00 11 11 11 11 11 invalid FFEOh invalid 0013h 13위 <u>유</u> ලි invalid invalid Buffer R-code data Operation image of the output code generator 00 0131131 01EDh FE00h EDh 01h F6h 8 Buffer enable Output code Register 2 Register 3 Register 1 R-enable G-enable B-enable R-code G-code B-code

F1G.2

Code length memory

560 bits

F I G. 3

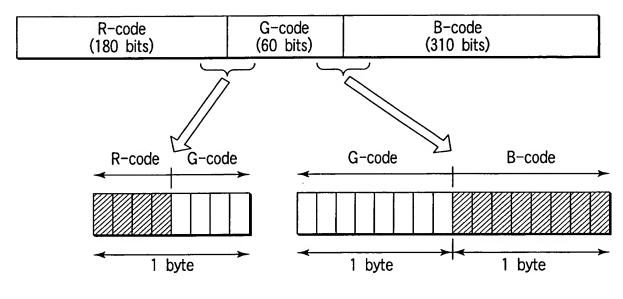
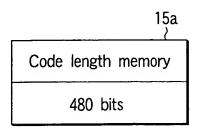


FIG.4



F I G. 5

	R-code (180 bits)	G-code (60 bits)	B-code (310 bits)
:	:	. :	:
N-6th	7 bits	8 bits	9 bits
N-5th	4 bits	5 bits	12 bits
N-4th	6 bits	10 bits	5 bits
N-3rd	13 bits	3 bits	8 bits
N-2nd	9 bits	8 bits	4 bits
N-1st	4 bits	6 bits	8 bits
Nth	10 bits	7 bits	3 bits

FIG. 6

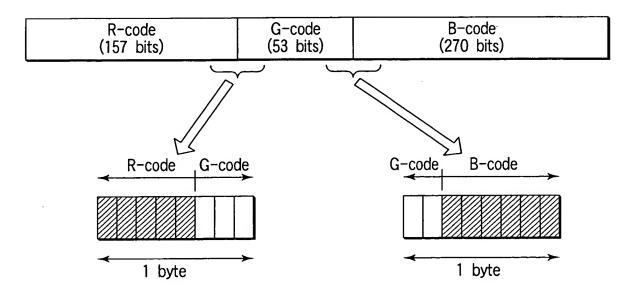


FIG. 7